

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**National Institute of technology, Arunachal Pradesh**  
 Class Routine for July-December, 2021

**M.TECH 1<sup>st</sup> Semester (VLSI)**

Time Days	9:00-10:00 AM	10:00-11:00 AM	11:00-12 Noon	12:00-1:00 PM	1:00-2:00 PM	2:00-3:00 PM	3:00-4:00 PM	4:00-5:00 PM
Monday		ECE 503 AJM	ECE 502 AM	MA – 502 DD	Lunch	ECE 504 SDR	ECE 506 SDR	
Tuesday	ECE 505 AJM						ECE 502 AM	ECE 504 SDR
Wednesday		MA – 502 DD	ECE 501 SMUK	ECE 502 AM		ECE 503 AJM	ECE 505 PSM	
Thursday			ECE 501 SMUK	MA – 502 DD		ECE 504 SDR	ECE 506 SDR	
Friday		ECE 505 PSM		ECE 503 AJM		ECE 501 SMUK		

Sl. No.	Subject Name	Subject Code	L	T	P	C	Course Instructor	Short name
1.	Probabilities and Random Process	MA - 502	3	0	0	3	Dr. Debjit Dutta	DD
2.	Physics of Semiconductor Devices	ECE 501	3	0	0	3	Dr. Subhadeep Mukhopadhyay	SMUK
3.	Digital CMOS IC	ECE 502	3	0	0	3	Dr. Alak Majumder	AM
4.	Analog IC Design	ECE 503	3	0	0	3	Dr. Abir J Mondal	AJM
5.	Embedded System & IoT	ECE 504	3	0	0	3	Dr. Sahadev Roy	SDR
6.	VLSI Lab I	ECE 505	0	0	8	4	Dr. Abir J Mondal Dr. Preetisudha Meher	AJM PSM
7.	Embedded System Design Lab I	ECE 506	0	0	4	2	Dr. Sahadev Roy	SDR