

NATIONAL INSTITUTE OF TECHNOLOGY
 (Established by Ministry of Human Resource Development, Govt. of India) Jote,
 District Papum Pare, Arunachal Pradesh – 791113
Department of Electronics & Communication Engineering

2nd Semester VLSI Class Routine (January - June 2025)

Day/Time	09AM-10AM	10AM - 11AM	11AM - 12PM	12PM – 01PM	01PM – 2PM	02PM - 03PM	03PM – 04PM	04PM – 05PM
MON				ECE 522 PSM		ECE 527 AM	ECE 509 SDR	
TUES				ECE 522 PSM		ECE 507 AJM	ECE 509 SDR	
WED	ECE 523 SDR		ECE 508 AJM/AM			ECE 507 AJM	ECE 509 SDR	
THURS	ECE 523 SDR	ECE 508 AJM/AM		ECE 527 AM		ECE 507 AJM	ECE 509 SDR	
FRI	ECE 523 SDR	ECE 522 PSM	ECE 527 AM					

Sl. No.	Subject Code	Subject Name	L	T	P	C	Course Instructor	Short name
1.	ECE 507	Mixed Signal IC Design	3	0	0	3	Dr. A. J. Mondal	AJM
2.	ECE 523	Embedded Systems for Industrial Automation	3	0	0	3	Dr. S. Roy	SDR
3.	ECE 527	VLSI System Design	3	0	0	3	Dr. A. Majumder	AM
4.	ECE 522	Memory Design & Testing	3	0	0	3	Dr. P. Meher	PSM
5.	ECE 508	VLSI Lab II	0	0	4	2	Dr. A. J. Mondal / Dr. A. Majumder	AJM / AM
6.	ECE 509	Embedded Systems Design Lab II	0	0	8	4	Dr. S. Roy	SDR