



NATIONAL INSTITUTE OF TECHNOLOGY, ARUNACHAL PRADESH, YUPIA, Pin-791112

(Established by MHRD, Govt. of India)

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NIT(AP)/Senate/49/2016-2017

Date : 03/06/2017

MINUTES OF THE 12TH MEETING OF THE SENATE OF NATIONAL INSTITUTE OF TECHNOLOGY, ARUNACHAL PRADESH HELD AT BOARD ROOM OF NIT, GUEST HOUSE, JHALUKBARI, GUWAHATI, ASSAM ON 03/06/2017 (SATURDAY) AT 02:00 PM.

The following members were present in the meeting:

1. Prof. (Dr.) Sarungbam Birendra Singh, Chairperson Senate & Director Incharge, NIT Arunachal Pradesh
2. Prof. Surabhi Banerjee, Ex-Vice Chancellor, Central University of Orissa – Member
3. Dr. Mainak Mallik, HoD, CE – Member
4. Dr. M. K. Shome, HoD, MH – Member
5. Dr. P. K. Bandyopadhyay, Station In-charge-Invitee
6. Dr. U. K. Saha, DIC(E&A) - Invitee
7. Dr. Pinaki Chakraborty, DIC (R&D) - Invitee
8. Dr. A. K. Atta, DIC (P&D) - Invitee
9. Dr. N. K. Pramanik DIC (F&W) - Invitee
10. Dr. S. K. Metya, DIC (FW/F&A) – Invitee
11. Dr. A. Vanav Kumar DIC (SA) - Invitee
12. Shri Mohit Kr. De, Asst. Librarian - Invitee
13. Shri Taba Tadhe Goyang, Assistant Registrar (Acad) - Non-member Secretary

At the outset the Director i/c welcomes all the Senate members and invitees. Four members out of six were present and the rest are invitee members.

Agenda Item: 12.01: Confirmation of the Minutes of 11th Meeting of the Senate held on 26/08/2016 and Action Taken Report.

Decision: Minutes of the 11th meeting of the Senate were approved. Action taken report was noted and approved.

Agenda Item No. 12.02: Ratification of the Results of End Semester Examination, July-Dec, 2016 & Jan-June, 2017

The summary of the End Semester Results (July-Dec, 2016) & (Jan-June, 2017) are given below, however the details of End Semester Results (July-Dec, 2016) & (Jan-June, 2017) are placed in **Annexure -12.02a & 12.02.b** respectively.

The Senate may consider the proposal & like to accord approval.

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Summary of End Semester Result (July-Dec, 2016, UG)

First Year, First Semester, 2016-2020 Batch:

CGPA → Branch ↓	Below 5	5-6	6-7	7-8	8-9	9- 10	Total
BT	5	1	3	5	0	0	14
CE	5	4	5	7	5	0	26
CHE	4	3	3	2	2	0	14
CSE	5	2	2	4	5	1	19
ECE	4	4	3	6	1	2	20
EE	7	2	4	3	3	1	20
ME	5	4	3	4	5	2	23
Total	35	20	23	31	21	6	136

Second Year, Third Semester, 2015-2019 Batch:

CGPA → Branch ↓	Below 5	5-6	6-7	7-8	8-9	9- 10	Total
BT	0	2	10	7	1	0	20
CE	0	0	6	9	7	1	23
CHE	4	6	5	4	1	0	20
CSE	3	2	8	6	5	0	24
ECE	3	1	8	11	4	0	27
EE	1	1	6	7	6	0	21
ME	2	1	3	12	6	0	24
Total	13	13	46	56	30	1	159

Third Year, Fifth Semester, 2014-2018 Batch:

CGPA → Branch ↓	Below 5	5-6	6-7	7-8	8-9	9- 10	Total
BT	0	0	0	5	2	0	7
CE	0	0	3	8	5	2	18
CSE	0	0	3	8	6	2	19

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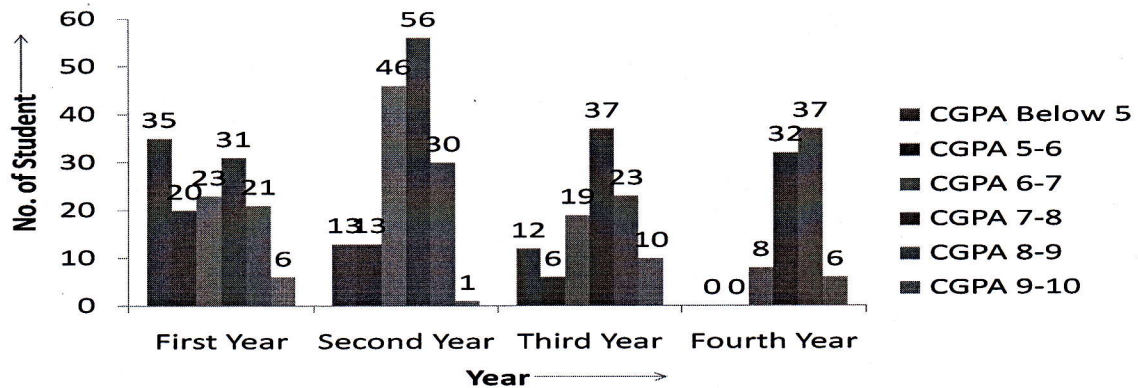
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ECE	4	4	3	6	1	2	20
EE	7	2	4	3	3	1	20
ME	1	0	6	7	6	3	23
Total	12	6	19	37	23	10	107

Fourth Year, Seventh Semester, 2013-2017 Batch:

CGPA → Branch ↓	Below 5	5-6	6-7	7-8	8-9	9- 10	Total
CE	0	0	0	4	13	1	18
CSE	0	0	4	6	6	1	17
ECE	0	0	2	4	8	0	14
EEE	0	0	1	10	3	1	15
ME	0	0	1	8	7	3	19
Total	0	0	8	32	37	6	83

End Semester Result (July-Dec, 2016, UG) (CGPA Wise)



End Semester Result (July-Dec, 2016, UG) (Category Wise/Gender Wise)

First Year/First Semester (2016-2020 Batch):

S/N	Branch	Qualified /GEN	Qualified /OBC	Qualified /ST	Qualified /SC	Qualified /PWD	Qualified /Girls	Qualified /Boys	Qualified /Total
1	BT	3/3	2/3	7/8	0/0	0/0	7/7	5/7	12/14
2	CE	5/5	4/5	12/12	4/4	0/0	7/7	18/19	25/26
3	CHE	1/1	2/2	10/10	1/1	0/0	2/2	12/12	14/14
4	CSE	5/6	5/5	6/7	1/1	0/0	3/3	14/16	17/19
5	ECE	6/6	1/1	10/10	3/3	0/0	6/6	14/14	20/20

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6	EE	3/4	4/4	9/10	2/2	0/0	4/4	14/16	18/20
7	ME	4/5	6/6	10/10	2/2	0/0	2/2	20/21	22/23
	G/Total	27/30	24/26	64/67	13/13	0/0	31/31	97/105	128/136

Second Year/Third Semester (2015-2019 Batch):

S/N	Branch	Qualified /GEN	Qualified /OBC	Qualified /ST	Qualified /SC	Qualified /PWD	Qualified /Girls	Qualified /Boys	Qualified /Total
1	BT	7/7	6/6	6/6	1/1	0/0	5/5	15/15	20/20
2	CE	2/2	9/9	8/8	3/3	1/1	2/2	21/21	23/23
3	CHE	9/9	5/5	4/4	1/2	0/0	4/4	15/16	19/20
4	CSE	9/9	7/8	2/2	3/3	2/2	3/3	20/21	23/24
5	ECE	11/11	8/8	3/3	4/4	1/1	4/4	23/23	27/27
6	EE	11/11	5/6	3/3	1/1	0/0	1/1	19/20	20/21
7	ME	6/6	8/8	4/4	5/5	1/1	0/0	24/24	24/24
	G/Total	55/55	48/50	30/30	18/19	5/5	19/19	137/140	156/159

Third Year/Fifth Semester (2014-2018 Batch):

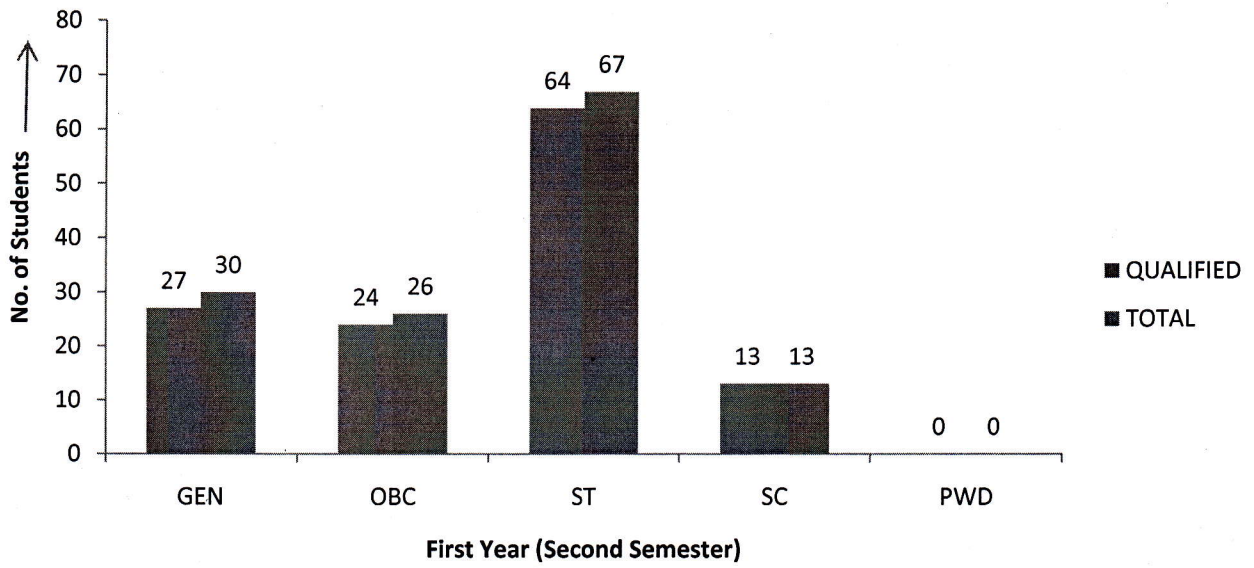
S/N	Branch	Qualified /GEN	Qualified /OBC	Qualified /ST	Qualified /SC	Qualified /PWD	Qualified /Girls	Qualified /Boys	Qualified /Total
1	BT	4/4	2/2	1/1	0/0	0/0	0/0	7/7	7/7
2	CE	2/2	4/4	8/8	3/3	1/1	3/3	15/15	18/18
3	CSE	8/8	5/5	2/2	4/4	0/0	5/5	14/14	19/19
4	ECE	10/10	4/4	2/2	3/3	1/1	3/3	17/17	20/20
5	EE	5/5	8/8	4/4	3/3	0/0	1/1	19/19	20/20
6	ME	7/8	10/10	2/2	3/3	0/0	1/1	21/22	22/23
	Total	36/37	33/33	19/19	16/16	2/2	13/13	93/94	106/107

Fourth Year/Seventh Semester (2013-2017 Batch):

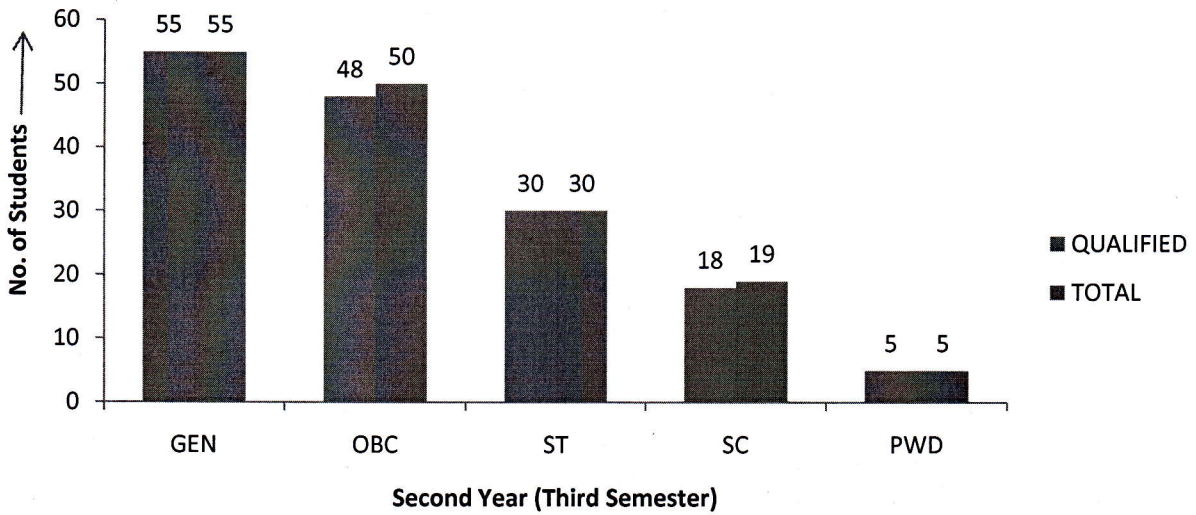
S/N	Branch	Qualified /GEN	Qualified /OBC	Qualified /ST	Qualified /SC	Qualified /PWD	Qualified /Girls	Qualified /Boys	Qualified /Total
1	CE	3/3	7/7	4/4	4/4	0/0	1/1	17/17	18/18
2	CSE	9/9	4/4	1/1	3/3	0/0	3/3	14/14	17/17
3	ECE	10/10	4/4	0/0	0/0	0/0	2/2	12/12	14/14
4	EEE	5/5	5/5	2/2	3/3	0/0	4/4	11/11	15/15
5	ME	8/8	7/7	1/1	3/3	0/0	0/0	19/19	19/19
	Total	35/35	27/27	8/8	13/13	0/0	10/10	73/73	83/83

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End Semester Result (July-Dec, 2016) (Category Wise)



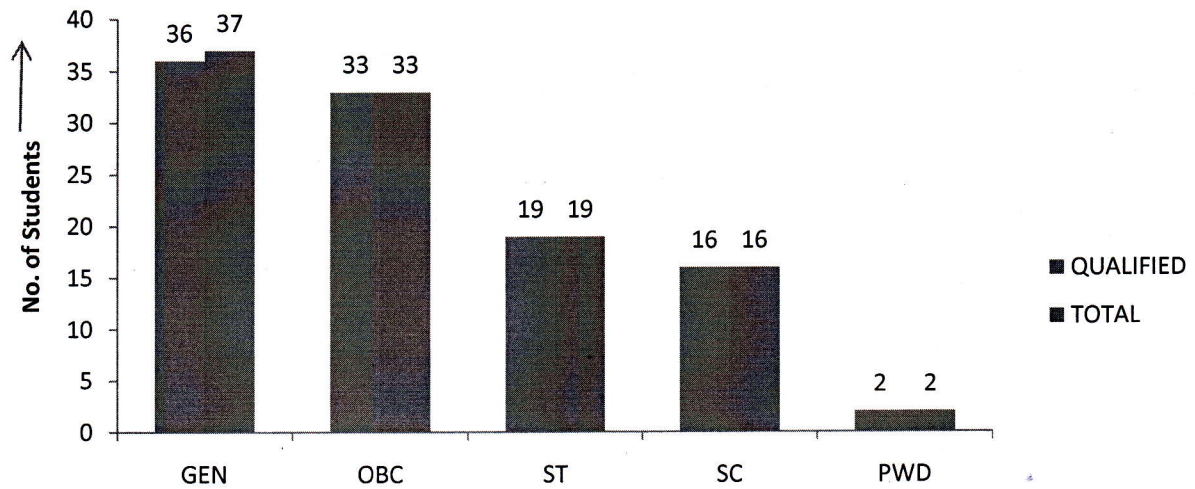
End Semester Result (July-Dec, 2016) (Category Wise)



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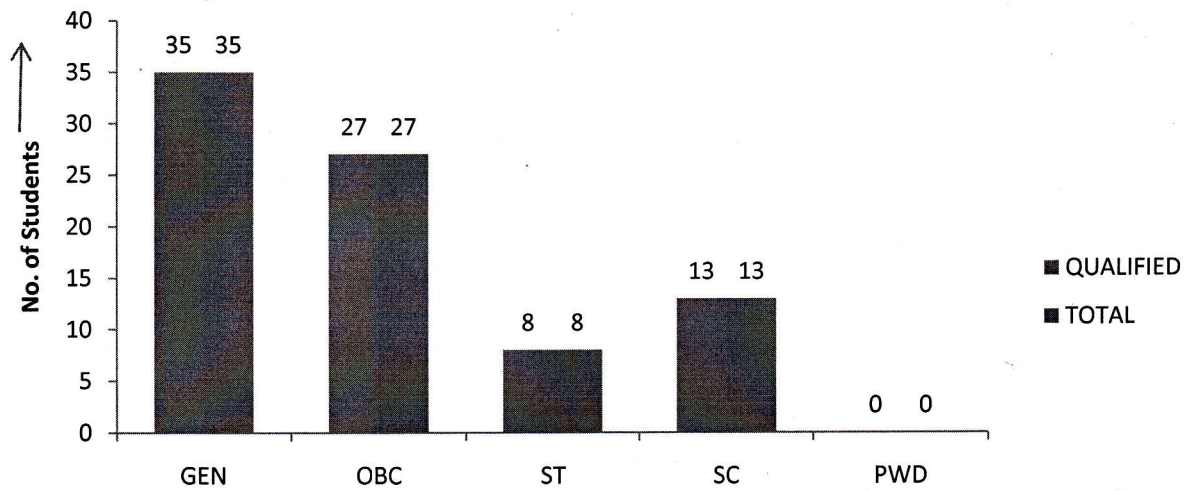
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End Semester Result (July-Dec, 2016) (Category Wise)



Third Year (Fifth Semester)

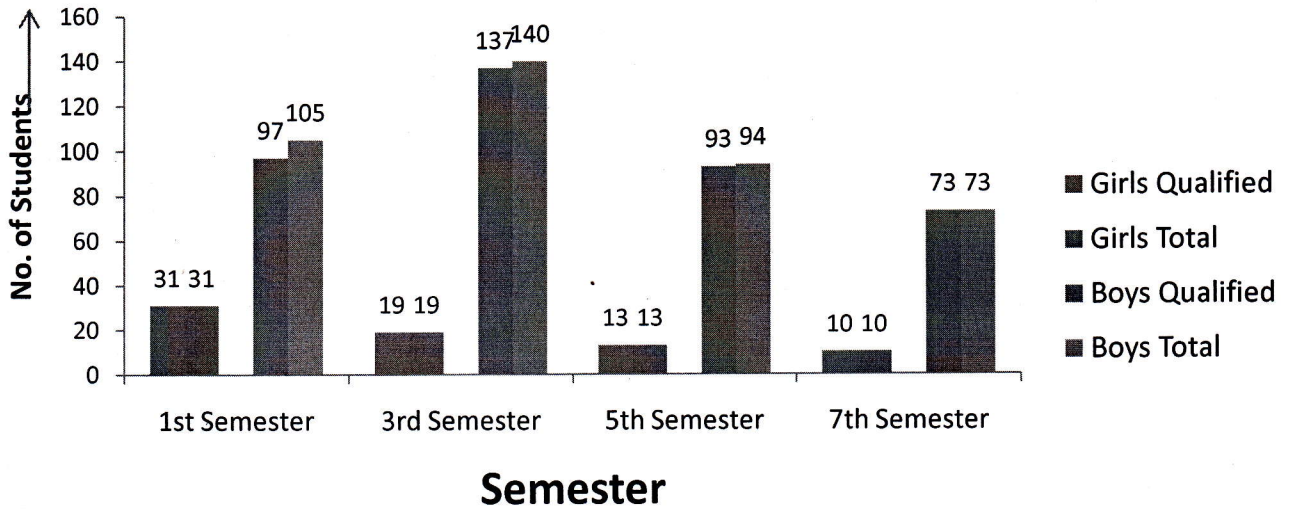
End Semester Result (July-Dec, 2016) (Category Wise)



Fourth Year (Seventh Semester)

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End Semester Result (July-Dec, 2016, UG) (Gender Wise)



Summary of End Semester Result (July-Dec, 2016, PG)

First Year, First Semester, 2016-2018 Batch:

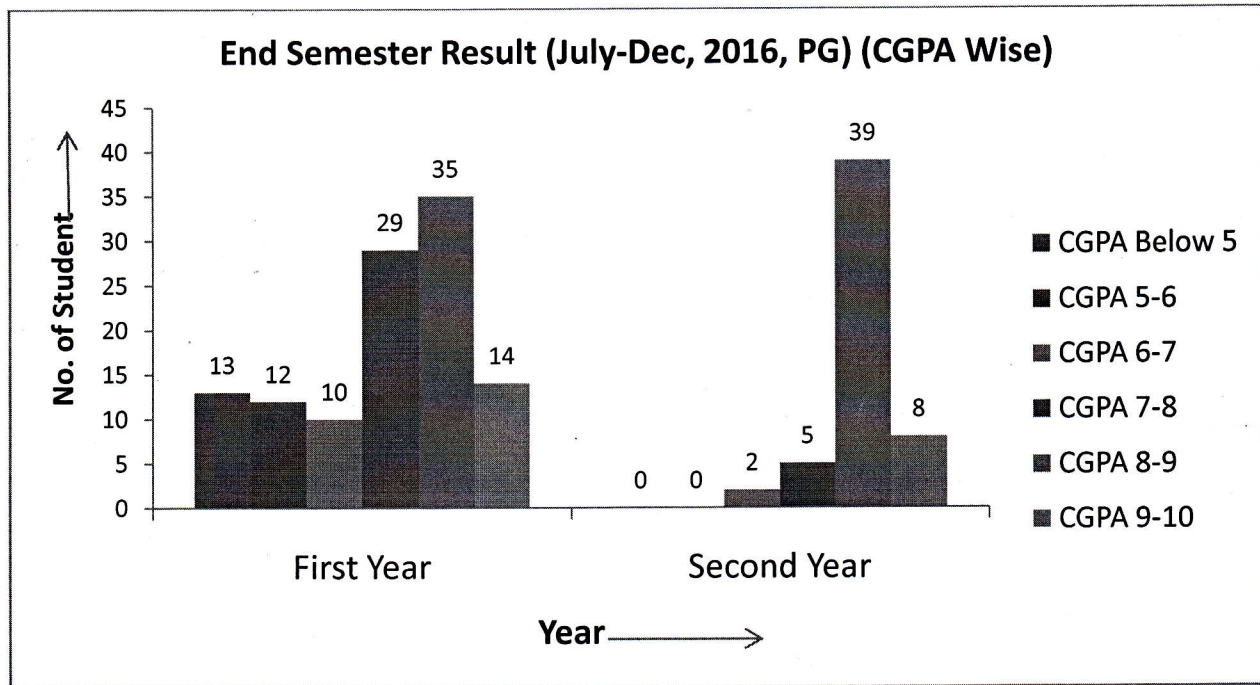
CGPA → Branch ↓	Below 5	5-6	6-7	7-8	8-9	9-10	Total
ATE	0	0	0	0	2	5	7
MTCS	1	5	1	5	4	1	17
EDM	0	0	0	2	4	1	7
MCC	0	1	1	5	3	1	11
VLSI	2	1	2	6	3	0	14
REEM	2	1	1	2	6	1	13
MSDIT	2	3	3	2	7	2	19
MC	0	0	0	3	0	2	5
MP	5	1	1	3	2	0	12
MRET	0	0	1	0	2	0	3
ES	1	0	0	1	2	1	5
Total	13	12	10	29	35	14	113

Second Year, Third Semester, 2015-2017 Batch:

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CGPA → Branch ↓	Below 5	5-6	6-7	7-8	8-9	9- 10	Total
ATE	0	0	0	0	3	1	4
EDM	0	0	0	1	12	1	14
MCC	0	0	0	0	11	1	12
MTCS	0	0	0	4	5	1	10
REEM	0	0	0	0	7	4	11
MC	0	0	0	0	1	0	1
MP	0	0	2	0	0	0	2
Total	0	0	2	5	39	8	54



End Semester Result (Jan-June, 2016, PG) (Category Wise/Gender Wise)

First Year/First Semester (2016-2018 Batch):

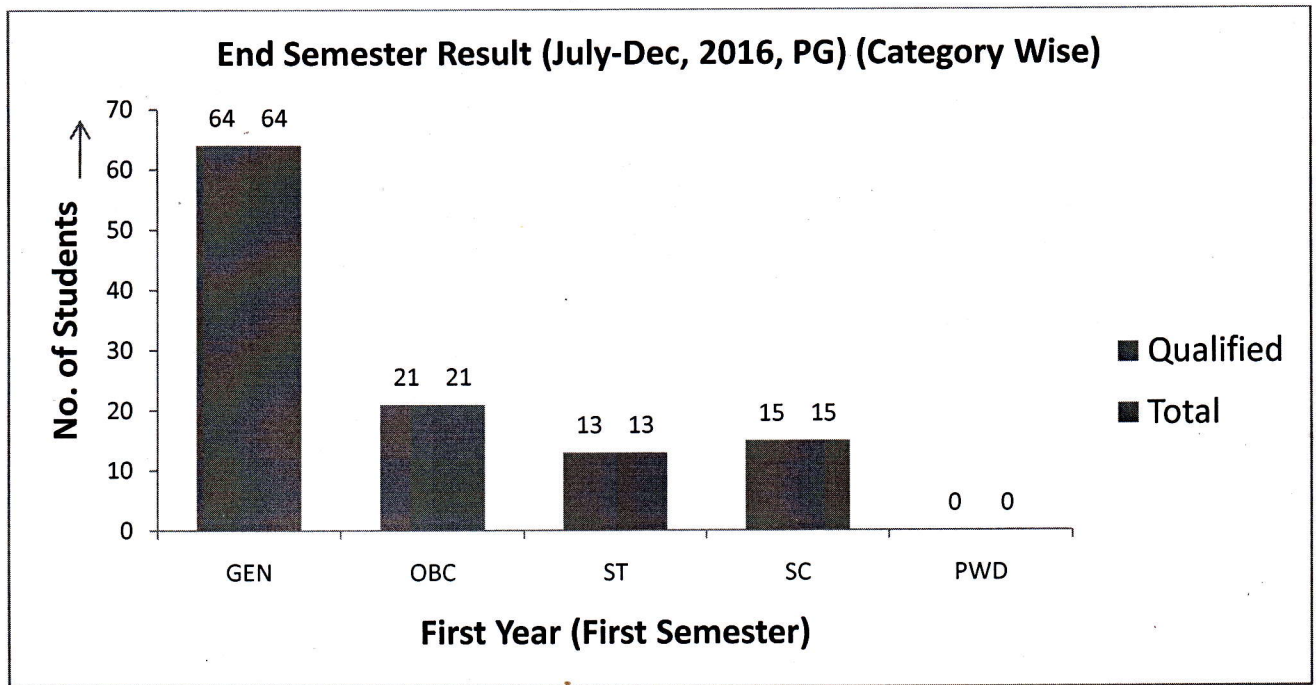
S/N	Branch	Qualified /GEN	Qualified /OBC	Qualified /ST	Qualified /SC	Qualified /PWD	Qualified /Girls	Qualified /Boys	Qualified /Total
1	ATE	4/4	1/1	1/1	1/1	0/0	2/2	5/5	7/7
2	MTCS	10/10	4/4	2/2	1/1	0/0	7/7	10/10	17/17
3	EDM	5/5	1/1	0/0	1/1	0/0	2/2	5/5	7/7
4	MCC	6/6	2/2	0/0	3/3	0/0	5/5	6/6	11/11
5	REEM	6/6	2/2	3/3	2/2	0/0	2/2	11/11	13/13
6	MSDIT	9/9	5/5	3/3	2/2	0/0	4/4	15/15	19/19
7	VLSI	8/8	3/3	1/1	2/2	0/0	8/8	6/6	14/14
8	MC	3/3	1/1	/0	81/1	0/0	5/5	0/0	5/5
9	MP	7/7	1/1	3/3	1/1	0/0	6/6	6/6	12/12
10	MRET	3/3	0/0	0/0	0/0	0/0	2/2	1/1	3/3

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11	ES	3/3	1/1	0/0	1/1	0/0	0/0	5/5	5/5
	G/Total	64/64	21/21	13/13	15/15	0/0	43/43	70/70	113/113

Second Year/Third Semester (2015-2017 Batch):

S/N	Branch	Qualified /GEN	Qualified /OBC	Qualified /ST	Qualified /SC	Qualified /PWD	Qualified /Girls	Qualified /Boys	Qualified /Total
1	ATE	3/3	0/0	0/0	1/1	0/0	0/0	4/4	4/4
2	MTCS	9/9	0/0	1/1	0/0	0/0	2/2	8/8	10/10
3	MCC	10/10	1/1	0/0	1/1	0/0	4/4	8/8	12/12
4	EDM	5/5	7/7	1/1	1/1	0/0	6/6	8/8	14/14
5	REEM	5/5	3/3	2/2	1/1	0/0	4/4	7/7	11/11
6	MC	1/1	0/0	0/0	0/0	0/0	0/0	1/1	1/1
7	MP	0/0	0/0	2/2	0/0	0/0	0/0	2/2	2/2
	G/Total	33/33	11/11	6/6	4/4	0/0	16/16	38/38	54/54



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Item: 12.03: Seeking Permission for enrollment of Ph. D from JRF (Sponsored Project).

As per recommendation of DRCs of various departments the following Junior Research Fellows of different sponsored projects are eligible to register for PhD from the academic session July-December 2017 as per governed by the rules position of NIT Arunachal Pradesh (**Annexure-12.03**).

Sl. No.	Name of the Scholar	Department	Sponsoring Agency	Area of Research	Supervisor(s)
1	Ms. Laxmi Linggi, JRF	Civil Engg	DST, SERB	Water Resources	Dr. M. Berlin Dr. Mainak Mallik (Co)
2	Shri Sanjib Kumar Das, JPF	Biotechnology	GBPI, MOEF	Plant Biotechnology	Dr. P. K. Hui
3	Shri Abhijit Paul, JRF	Mechanical Engg.	DST, SERB		Dr. Anup Paul Dr. D. Sen (Co)
4.	Shri Roushan Kumar	Mechanical Engg.	Institute Project/ Self Sponsored		Dr. Susanta Maity Dr. Dr. Dipak Sen

Senate may like to approve the same.

Decision: Candidate under serial no. 4 will not be considered for enrolment of Ph.D. As the candidate is not under the external funded sponsored project. Enrolment process of Ph.D may be proceed through interview for NET/ GATE qualified students and for without NET/GATE qualified candidates under sponsored project, written test is to be carried out at Departmental level (DRC).

Selection of Ph.D candidates under serial No. 1 to 3 may be carried out based on the above mentioned rule.

Item 12.04: Change of Syllabus in Civil Engineering Department

Proposal of change in syllabus is prepared in a meeting with the faculty members of the department and Prof. Somnath Ghosh of Jadavpur University. Copy of the MOM and revised syllabus and curriculum is attached. **Annexure-12.04**

Senate may like to approve the same.

Decision: Approved as proposed by the committee for change of syllabus in Civil Engineering Department.

Hereafter, the syllabus must be reviewed after every three years by the Board of Studies comprising of external members, industrial experts and internal members of the institute.

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Item 12.05: Seeking Permission for implementation of modified/revised rule for JRF/PhD students

For Ph.D/JRF students whose supervisor(s) left to other University / Institute may continue as an External Supervisor and another supervisor should be appointed/nominated from the Institute itself. If the students are having more than one supervisor they should continue as it is and for others, DRC should recommend and may put it in next Senate Meeting.

After end of every Semester all the Ph.D/JRF students should submit their progress report in a prescribed form as attached and should be submitted to examination cell after getting signature from all the relevant members of DRC/DC. **Annexure—12.05**

Senate may like to approve the same.

Decision: Approved.

Item 12.06: Seeking Permission for Engagement of Guest Faculty Members

For the session of July-Dec 2017 after maintaining teacher student ration, NIT-AP requires 27 Guest Faculty Members for all departments. **Annexure—12.06**

Senate may like to give the suggestion for the same.

Decision: Senate approved that the present remuneration for Guest faculty may be revised to the maximum of @ Rs. 38,000/- per month @ Rs. 1000/- per hour. The number of Guest faculty for civil to be increased from 4 (four) to 5 (five).

Item 12.07: Seeking Permission for Advertisement of full time (Institute Fellowship)/Part time Ph.D. programme in all the Departments.

Following are the requirements for full time (Institute Fellowship)/Part time Ph.D. programme in all the various Departments for the upcoming sessions which need to be advertised in due time. **Annexure—12.07**

Departments	Full Time-scholar (No.)	Part time scholar
Civil Engineering	2	2
Electronics & Computer Engineering (CSE + ECE)	3 +3	6
Electrical Engineering	03	0
Mechanical Engineering	02	02
Biotechnology & Chemical Engineering	02	02
Basic and Applied Science	6+2+2+1	22
Management and Humanities	02	02
Total	28	36

1st two years the financial involvement for 28 full time Ph.D Scholars will be = Rs. 1,68,00000.00

3rd year the financial involvement for 28 full time Ph.D Scholars will be = Rs. 94,00000.00

Senate may like to approve subject to availability of fund from MHRD.

Decision:. Rules and Regulations along with draft copy of advertisement for part time Ph.D. enrolment for all departments may be put up to the Chairman, Senate, NIT, Arunachal Pradesh for approval. Dr. Mainak Mallik, Associate Professor, HOD, Civil Engineering, NITAP will prepare the proforma for advertisement dully signed by all internal members of the Senate.

For full time Institute fellowship, the case was referred to Senate Chairman, NIT Arunachal Pradesh

The Chairman, Senate will take necessary decision.

Item 12.08: Seeking approval for change of subject code, HSS credit and subject reduction.

(i) The subject code MAS 901 for advanced Engineering Mathematics in M.Tech may be changed to MAS 910.

(ii) HSS credit and subject reduction: Management subjects may be reduced to minimum number as total number of Management subjects in B.Tech is eleven (11) i.e. 20 Credits (out of 210 Credits) in addition of basic science and other branches subjects. The basic science and other branches subjects in the curriculum are fourteen (14) i.e. 44 Credits. The Mechanical Engineering Dept. also having Industrial Engineering & Management subject (ME 506). Due to which it is unable to give some important core subjects as already 210 Credits have been reached. **Annexure-12.08**

Senate may like to give appropriate comment for the same as proposal of HSS credit and subject reduction from the department of ME already vetted by the previous senate of NIT-AP.

Decision:

The change of subject code is approved.

HSS credit and subject reduction: To be put up after reviewing through Board of Studies, NITAP.

Item 12.09: Seeking Approval for part time PhD for Institute employee

As per recommendation of DRC the following candidates (also employee of the Institute) are permitted to take admission for PhD in the academic session started with 2017-18. The recommendation of DRCs is placed at **Annexure—12.09.**

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Sl. No	Name of the Candidates	Department	Supervisor(s)
1	Sri Taba Tadhe Goyang	Management & Humanities	Dr. M. Momocha Singh & Dr.(Prof.) C.T.Bhunia

Senate may like to approve for the admission for part time Ph.D.

Decision: To be put up to the Chairman, Senate, NITAP by Chairman DRC of Management & Humanities by referring the MHRD / UGC rules for selection of Ph.D supervisor.

Item 12.10: Award of Ph.D Degree.

- A. All reports (Indian, foreign thesis reports as well as viva –voce reports) of Shri Subhasish Benerjee and Shri Anish Kumar Saha are placed in a confidential envelope for considerations of the award of PhD (Engg) Degree [**Annexure - 12.10**].
Senate may like to approve the Award of PhD Degree to Shri Subhasish Benerjee and Shri Anish Kumar Saha .
- B. Thesis adjudication reports (both Indian and foreign as well as supervisor report) of Sri Sahadev Roy, Sri Koj Sambyo, Sri Manas Pratim Dutta and Smti. Yaka Bulu have been received and placed in a confidential envelop for consideration to conduct viva-voce on 01.08.2017 by the following experts:
The proposed panel of experts for conducting viva voice is as under:
a) Prof. M. S. Gour, Professor CSE, MNIT, Jaipur, b) Prof. B. P. Sinha, Professor, ISI Kolkata, c) Prof. Roy Pillai, Professor, EEE, IIT Gauhati and d) supervisor Prof. C. T. Bhunia , Former Director, NIT-AP.
- C. Some Reports of evaluation of Ph. D thesis in respect of Smti. Yang Saring is due. If the reports are available in acceptable criteria before the viva-voce as mentioned in B above, Senate may allow the candidate to appear before the same board for defence.

Senate may like to approve to same.

Decision:

1. A Institute Research Council (IRC) to be constituted with the approval of Chairman, Senate.
2. All reports to be put up after reviewing by the Institute Research Council (IRC).
3. All the names of the external experts may be put to the Chairman, Senate confidentially for approval.

The above matter to be dealt by the Station-in-charge of NIT Arunachal Pradesh.

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Item 12.11: Consideration of Ph.D thesis submission of two scholars: Two Ph. D scholars of this Institute (they also faculty members of this Institute) are applying to get permission for their Ph. D thesis submission. As per RR of Ph.D of NIT Arunachal Pradesh, the candidate has to fulfil the following clauses (**Annexure-12.11 (i)**).

i. A registered candidate shall have to work for a minimum period of two years after registration. If the Supervisor certifies that the candidate has carried out research for two years and that the thesis embodies at least two years' work, then the Doctorate Committee may permit a candidate to submit his/her thesis one year after the successful completion of Coursework.

ii. A student must have at least 3 (three) publications in a refereed journal out of his/her doctoral research work prior to submission and shall produce evidence for the same in the form of acceptance letter or offprint to the Doctorate Committee.

iii. Before final submission of the thesis a student who has satisfactorily completed the course works and must present his/her work at a meeting of the Ph.D. Research Committee (BoR & DC). Interested researchers may also attend the meeting as invitee. Any suggestion may be suitably incorporated under the advice of the supervisor(s).

The details about the above clause for individual scholar is hereunder Annexure-12.11 (ii) & (ii):

Name of the Student: Shri Alak Majumder Title of the Thesis: Process Variation Aware Serializer and Data Reconstruction Architecture for ≥ 25 Gbps On-chip Data Links. Name of the supervisors: Dr. Pinaki Chakraborty, Asst. Professor, NIT AP & Prof. B. K. Bhattacharyya (<i>Fellow IEEE</i>), NIT Agartala			
Date. Of Registration	Status of course work	List of Publication	Remarks
03.09.2014	Completed	A. Majumder, B.K. Bhattacharyya, "Reconstruction of single pulse originally having 40psec width coming from a lossy and noisy channel in point to point interconnect", <i>Turkish Journal of Electrical Engineering and Computer Sciences</i> - In Press. (SCIE)	<u>As per Guideline of RR</u> As per the Clause No. 9.1(i) 2 years are over after Ph. D Registration
		A. Majumder, A.J. Mondal, B.K. Bhattacharyya, "Threshold Adjustment of Receiver Chip to Achieve a Data Rate > 66 Gbit/Sec in Point-to-point Interconnect", <i>Integration, The VLSI Journal; Elsevier</i> -In Press. (SCI)	
		B. Nath, A. Majumder, M. Das, A.J. Mondal, P. Chakraborty, B.K. Bhattacharyya, "Voltage Keeper Based 28.27μ W New Frequency Divider Circuit in 90nm Technology for	As per the Clause No. 9.1(ii) the

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		<p>Gigascale SerDes Application”, <i>IEEE VLSI Circuit and System Letters</i> – Accepted (Peer Reviewed)</p> <p>A. J. Mondal, A. Majumder, B.K. Bhattacharyya, “Mathematical Formulation to Design and Implementation of a Low voltage swing transceiver circuit”, <i>VLSI Integration, Elsevier-In Press.(SCI)</i></p> <p>A. Majumder, B. Nath, M. Das, B.K. Bhattacharyya, “Analysis of performance metrics of a process variation aware current mode low swing on-chip interface circuit switching at GHz ranges”, <i>Journal of Circuits, Systems and Computers</i> -Under Review (SCI)</p> <p>A. Majumder, A.J. Mondal, B.K. Bhattacharyya, “A 65nm Design of 0.6V/8.98μW PVT Aware Dynamic Analog Comparator for High Speed Data Reconstruction Applications”, <i>ASP Journal of Low Power Electronics</i> -Under Review (SCOPUS, E-SCI)</p> <p>A. Majumder, M. Das, B.K. Bhattacharyya, “A 90nm Design of PVT Aware Current Mode Single Ended 8:1 Mux Serializes Data up to 44.44Gbit/sec”, <i>IEEE Transaction on VLSI Systems</i>- Ready For Submission (SCI)</p>	<p>candidate has more than 3 (three) publications in referred Journal.</p> <p>As per Clause No. 9.1(iii) the candidate has completed his course work.</p>
		<p>A. Majumder, M. Das, B. Nath, A.J. Mondal, B.K. Bhattacharyya, “Design of Low Noise High Speed Novel Dynamic Analog Comparator in 65nm Technology”, <i>26th Conference on RADIOELEKTRONIKA 2016, Kosice, Slovakia. (Peer Reviewed)</i></p> <p>A. Majumder, A.J. Mondal, V. Chaudhary, B.K. Bhattacharyya, “A Methodology to Achieve Over 25Gbit/s data rate in Point to Point Interconnect”, <i>11th International conf. on Microwave, Antenna, Propagation & Remote Sensing, IEEE-GRSS, Jodhpur, India. (Peer Reviewed)</i></p> <p>A. Majumder, B. Chowdhury, V. Chaudhary, P. Chakraborty, B.K. Bhattacharyya, “A Methodology of High Speed Signaling through</p>	<p>Conference paper</p>

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	strip-line Interconnect using Resistive Channel to Minimize ISI Noise”, <i>11th International conference on Microwave, Antenna, Propagation & Remote Sensing, IEEE-GRSS, Jodhpur, India. (Peer Reviewed)</i>	
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Name of the Student: Shri Abir J Mondal
Title of the Thesis: A Variation Aware Transceiver for Point to Point Interconnects
Name of the supervisors: Dr. Pinaki Chakraborty, Asst. Professor, NIT AP & Prof. B. K. Bhattacharyya (*Fellow IEEE*), NIT Agartala

Date. Of Registration	Status of course work	List of Publication	Remarks
03.09.2014	Completed	Abir J Mondal, Alak Majumder and Bidyut K Bhattacharyya, “A Mathematical Formulation to Design and Implementation of a Low Voltage Swing Transceiver Circuit”, Integration, The VLSI Journal- Elsevier. <u>In Press (SCI)</u>	<p><u>As per Guideline of RR</u></p> <p>As per the Clause No. 9.1(i) 2 years are over after Ph.D Registration</p> <p>As per the Clause No. 9.1(ii) the candidate has 3 (three) publications in referred Journal.</p> <p>As per Clause No. 9.1(iii) the candidate has completed his course</p>
		Sandeep Kr Singh, Abir J Mondal and Alak Majumder, “Generation and Performance Evaluation of Reconfigurable Fault Tolerant Routing Algorithm for 2D Mesh NoC”, Procedia Computer Science- Elsevier, Vol. 57, pp. 232-240, 2015. (Scopus)	
		Paromita Bhattarjee, Abir J Mondal and Alak Majumder, “A Graphical Approach to Design and Optimization of MOS Amplifier”, Journal of Engineering Science and Technology. <u>Revision submitted (Scopus)</u>	
		Alak Majumder, Abir J Mondal, and Bidyut K Bhattacharyya, “A Mathematical Formulation to Design and Implementation of a Low Voltage Swing Transceiver Circuit”, Integration, The VLSI Journal- Elsevier. <u>In Press (SCI)</u>	
		B Nath, Alak Majumder, M Das, Abir J Mondal, Pinaki Chakraborty and Bidyut Kumar Bhattacharyya, “Voltage Keeper Based 28.27 μ W New Frequency Divider Circuit in	

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	<p>90 nm Technology for Gigascale SerDes Application”, IEEE VLSI Circuits and Systems Letter. <u>Accepted (Peer Review)</u></p> <p>Abir J Mondal, Aditya S. Sengupta, Alak Majumder and B K Bhattacharyya, “Mathematical Framework to Design and Implementation of a Bidirectional Transceiver. (Under Review)</p> <p>Abir J Mondal, Alak Majumder, Bidyut K. Bhattacharyya and Pinaki Chakraborty, “A Process Aware Delay Circuit with Reduced Impact of Input Switching at GHz Frequencies”. (Under Review)</p> <p>Alak Majumder, Abir J. Mondal and Bidyut K. Bhattacharyya, “A 65nm Design of 0.6V/8.98μW PVT Aware Dynamic Analog Comparator for High Speed Data Reconstruction Applications”. (Under Review)</p> <p>Alak Majumder, M. Das, Abir J. Mondal, B.K. Bhattacharyya, “A 90nm Design of PVT Aware Current Mode Single Ended 8:1 Mux Serializes Data up to 44.44Gbit/sec”. (Ready For Submission)</p>	work.
	<p>Paromita Bhattcharjee, Abir J Mondal, Alak Majumder and Sanjeev K Metya, “Amplifier Design and Optimization Using Non Linear Programming”, 26th IEEE International Conference Radioelektronika, 2016, Kosice, Slovakia. (Peer Reviewed)</p> <p>Alak Majumder, M. Das, B. Nath, Abir J Mondal and B.K. Bhattacharyya, “Design of Low Noise High Speed Novel Dynamic Analog Comparator in 65nm Technology”, 26th IEEE International Conference Radioelektronika, 2016, Kosice, Slovakia. (Peer Reviewed)</p>	<u>Conference paper</u>

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	Paromita Bhattacharjee, Abir J Mondal and Alak Majumder, "A Constraint Driven Technique for MOS Amplifier Design", 20 th IEEE International Symposium on VLSI Design Test, 2016, IIT Guwahati, India. (Peer Reviewed)	
	Rama P Acharya, Abir J Mondal and Alak Majumder, "A Method to Design a Comparator for Sampled Data Processing Applications", .20 th IEEE International Symposium on VLSI Design Test, 2016, IIT Guwahati, India. (Peer Reviewed)	
	Alak Majumder, Abir J. Mondal , V. Chaudhary and B. K. Bhattacharyya, "A Methodology to Achieve Over 25Gbit/s data rate in Point to Point Interconnect", 11 th IEEE ICMARS, 2015, Jodhpur, India. (Peer Reviewed)	

The details are submitted about the Ph. D work progress (acceptance letter or offprint and SCI, Scopus indexing) of various scholars of the Institute, Senate may like to see and accord approval for

(1) pre submission seminar on 12.06.2017 by the three experts (i) Prof. S. Sen, IRP & E, University of Calcutta, (ii) Prof. Gourav Trivedi, IIT Guwahati, (iii) Prof. S. Dhar, University of Calcutta (iv) Any one from IIT G or NERIST]

(ii) Change of thesis title

(iii) Finalization of list of examiners for thesis adjudication (sealed envelope) (one Indian and one outside the country).

[DIC (R&D) dissociate himself as the scholars are under the Guidance of DIC (R&D). So, DIC (E&A) is authorised to take the charge till the complete official procedure for awarding PhD degree]

Decision: It was decided not to submit such cases in the Senate meeting. DIC(E & A) will take the charge of the above proceedings for further process.

Item 12.12: The change of supervisor of Sri Gomar Bam, EE Department integrated PhD.

Sri Gomar Bam, JE of NIT Arunachal Pradesh was permitted to do integrate PhD under the guidance of Dr. D Sen, Assistant professor, Mechanical Engineering and Dr. S. Maity, Assistant professor, Electronics & Communication Engineering as there was no supervisor in the

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Department of Electrical Engineering. Now there is a sufficient eligible supervisor in the department of Electrical Engineering. The two previous supervisors Dr. Sen and Dr. S. Maity have submitted the NOC for doing the intergraded PhD under Dr. Abik Banerji, Assistant Professor of Electrical Engineering. DRC of Electrical Engineering has also recommends that Dr. Abhik Banerji, Assistant Professor of Electrical Engineering may be the supervisor of Sri Gomar Bam [Annexure- 12.12].

Senate may like to approve the change of supervisor for Sri Gomar Bam.

Decision: Approved

Item 12.13: Proposal for MOU with other institute for M.Tech in Appropriate Technology and Entrepreneurship Programme

National Institute of Technology- Arunachal Pradesh is running a unique PG course on Appropriate Technology and Entrepreneurship from 2013-15 onwards in collaboration with National Institute of Rural Development and Panchayati Raj (NIRD &PR), Ministry of Rural Development, Govt. of India, Hyderabad. There have been some execution problems because of availability of faculty members, Lack of infrastructure, proper Lab/ workshop facilities etc. The program is very unique and also one of its kind in line with the 'start Up and stand Up', and 'make in India' policies of Central Govt. of India. This course is first introduced at PG level by NITAP in India.

In view of the above, it is proposed that efforts need to be made to run this program more efficiently and effectively. In this regard, collaboration with other organization such as National Institute of Entrepreneurship at Guwahati, Kolkata and other nearby institutes like IIMs may be explored for proper execution and running the course.

Further, approval is required for fund for M. Tech in Appropriate Technology and Entrepreneurship students to attend seminar, workshop etc to get the practical experience. Without seminars/ workshops students are not getting practical experience. **Annexure-12.13**

Senate may like to approve.

Decision: Head, Department of Management and Humanities to put up the draft MOU to the Chairman, Senate for approval.

Item 12.14: Seeking Permission for Continuation of MS Programme

As per 17th BoG of NIT Arunachal Pradesh under agenda item 17.5, following six courses will keep abeyance for academic session 2017-18 and 2018-19 due to infrastructural issues.

- a) M.Tech in Electronics Design and Manufacturing.
- b) MS in Mathematics and Computing
- c). MS in Mathematical Research in Engineering and Technology.
- d) MS in Electronic Science
- e) Online MBA.
- f) Online M. Phil.

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Name of the faculty	Dean ship	Duration	Remarks
Dr. Pinaki Chakraborty, Asst Prof, Physics	Research & Development	01.12.2011 to till date: 5.5 years	Other duties like CHN (LC), Prof in Charge EDC, FAA, Ex-HoD (twice)
Dr. U. K. Saha, Asst. Prof. Mathematics	Examination & Academic	01.01.2014 to till date 3.5 years	Ex-HoD, Chairman routine preparation committee.
Dr. A. K. Atta, Asst. Prof. Chemistry	Planning & Development	23.01.2015 to till date .2.5 years	Ex-HoD
Dr. Sanjeev K. Metya, Asst. Prof. ECE	Finance & Audit	06.06.2016 to till date	
Dr. A Vanav Kumar, Asst. Prof. Mathematics	Students Affairs	06.06.2016 to till date	

Proposed Deans:

Name of the faculty	If any duties assign to him (presently)	Date of Joining
Dr. M. K. Shome, Associate Prof. MH	HOD/MH	24.12.2013
Dr. M. K. Mallik, Associate Prof., CE	HOD/CE & CHN (CC) As per 18 th BWC meeting decision: HoD, Civil Engineering NIT-A.P. (Chairman of Construction Committee by position).	22.12.2015
Dr. P. K. Bandhopadhyay, Associate Prof., ECE	Station –in--charge	
Dr. S. K. Chakraborty, Asst. Prof. CSE	HoD/CSE & ECE	01.12.2011
Dr. R. S. Goswami, Asst. Prof, ECE	Network Administrator	01.12.2011
Dr. K. R. Singh, Asst. Prof. Mathematics	NIL	31.01.2012
Dr. R. Pudur, Asst. Prof., EE	HOD, EE	01.02.2012
Dr. Anish Saha, Asst. Prof., CSE	NIL	03.10.2012
Dr. S. Banerjee Asst. Prof. CSE	Web Master	27.02.2013
Dr. P. K. Swain Asst. Prof. Physics	HoD, BAS	18.10.2013
Dr. S. Maity Asst. Prof. Mathematics	Chairman of PC	16.09.2013
Dr. Deepak sen	HoD, ME	31.01.2015

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 Swain 16/6/17
 Maity 16/6/17
 Deepak sen 16/6/17

Asst Prof. ME		
Dr. Kimjolly Lhouvum	HoD, BT&CHE	09.03.2015
Dr. M. D. Ghatak, Asst. Prof. ME	NIL	09.03.2015
Dr. T. D. Das Asst. Prof. Electronic Science	NIL	20.04.2015
Dr. P. K. Hui, Asst. Prof. BT	NIL	24.06.2015
Dr. Saikat K Jana, Asst. Prof. BT	NIL	21.07.2015
Dr. M. Berlin, Asst. Prof., CE	NIL	07.12.2015
Dr. A. Banerjee, Asst. Prof., EE	NIL	22.12.2015
Dr. Anup Pal, Asst. Prof. ME	NIL	30.12.2015
Dr. Sakti Prasad, Asst. Prof. Mathematics	NIL	05.01.2016
Dr. Rajib Jana, Asst. Prof.	NIL	21.01.2016
Dr. S. Mukhopadhyay, Asst. Prof., ECE	NIL	22.01.2016
Dr. M. M. Singh Asst Prof. Management	NIL	08.02.2016
Dr. Deepak Gupta, Asst. Prof. CSE	NIL	12.02.2016
Dr. K. Vijayakumar Asst Prof. Humanities	NIL	27.02.2016
Dr. Debjit Dutta, Asst. Prof. Mathematics	NIL	18.07.2016
Dr. K. Mandal Asst Prof. Chemical Engineering	NIL	27.05.2016
Dr. P. K. Mohanty, Asst. Prof., ME	NIL	01.08.2016
Dr. P. Mehar Asst. Prof. CSE	NIL	18.07.2016

The Senate may consider the proposal & like to accord approval.

Decision: Chairman Senate instructed to Station-in-charge to put this matter to the Director, NIT Arunachal Pradesh.

Item 12B: Leave rules for attending Conferences/Seminars/Workshop

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