

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
 NATIONAL INSTITUTE OF TECHNOLOGY, ARUNACHAL PRADESH
 CLASS ROUTINE FOR JULY-DEC, 2022
 SECOND SEMESTER (M.TECH.)

Department of ECE, VLSI – 1th Year

Day/Time	09AM - 10AM	10AM - 11AM	11AM - 12PM	12PM – 01PM	01PM – 02PM	02PM - 03PM	03PM – 04PM	04PM – 05PM
MONDAY	X	X	X	X		ECE – 523 SDR		ECE – 522 PSM
TUESDAY	X	ECE – 527 AM		ECE – 522 PSM		ECE – 509 SDR		
WEDNESDAY	X	ECE – 527 AM	ECE – 507 AJM			ECE – 509 SDR		
THURSDAY	X	ECE – 522 PSM	ECE – 523 SDR	ECE – 507 AJM		X	X	X
FRIDAY	ECE-505 AJM					ECE – 509 SDR		X

Sl. No.	Subject Name	Subject Code	L	T	P	C	Course Instructor	Short Name
1	Mixed Signal IC Design	ECE – 507	3	0	0	3	Dr. Abir Jyoti Mondal	AJM
2	Memory Design & Testing	ECE – 522	3	0	0	3	Dr. Preetisudha Meher	PSM
3	Embedded Systems for Industrial Automation	ECE – 523	3	0	0	3	Dr. Sahadev Roy	SDR
4	VLSI System Design	ECE – 527	3	0	0	3	Dr. Alak Majumder	AM
5	VLSI Lab II	ECE – 508	3	0	0	3	Dr. Abir Jyoti Mondal	AJM
6	Embedded Systems Design Lab II	ECE – 509	0	0	8	4	Dr. Sahadev Roy	SDR